



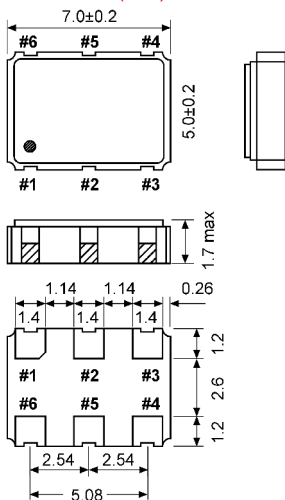
LVDS VCXO

SMD-version

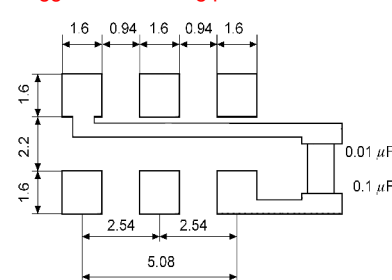
2.5 / 3.3V

part no.	12.xxxxx
model	KXO-V63
frequency range	20.0 ~ 700.0 MHz
frequency stability incl. temperature stability input voltage and load stability, aging.	$\pm 25\text{ppm} \sim \pm 100\text{ppm}$ over $-20^\circ \sim +70^\circ\text{C}$ (referred at 25°C) $\pm 25\text{ppm} \sim \pm 100\text{ppm}$ over $-40^\circ \sim +85^\circ\text{C}$ (referred at 25°C)
output load	100 Ohm
operating temperature	standard $-20^\circ \sim +70^\circ\text{C}$ available $-40^\circ \sim +85^\circ\text{C}$ (=KXO-V63T)
storage temperature	$-40^\circ \sim +125^\circ\text{C}$
input voltage	+2.5V DC $\pm 5\%$ or + 3.3V DC $\pm 5\%$
input current	45 mA typ., 60 mA max.
start up time max.	10 ms
symmetry	50% of waveform
rise time (Tr)	400ps typ 850ps max. (20% ~ 80% of waveform)
fall time (Tf)	400ps typ 850ps max. (80% ~ 20% of waveform)
disable delay time max.	200 ns
enable delay time max.	4 ms
"0" level max.	0.9V ~ 1.1V
"1" level min.	1.43V ~ 1.6V
LVDS offset output voltage	1.125V ~ 1.375V
frequency adjustment (pullability)	$\pm 50\text{ppm}$, $\pm 100\text{ppm}$
tristate function	yes
phase jitter (12kHz ~ 20MHz)	RMS: 1ps typ, 3ps max.
typical phase noise	-70 dBc/Hz at 10 Hz -105 dBc/Hz at 100 Hz -130 dBc/Hz at 1 kHz -145 dBc/Hz at 10 kHz -145 dBc/Hz at 100 kHz -145 dBc/Hz at 1 MHz
contents of reel	1000 pcs.

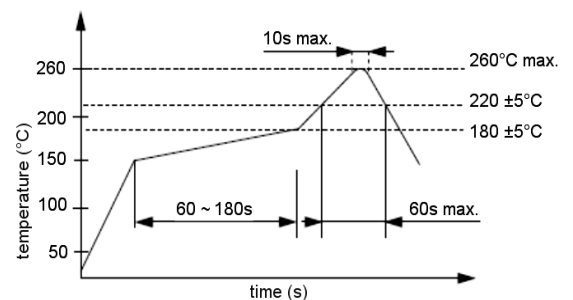
Dimensions (mm):



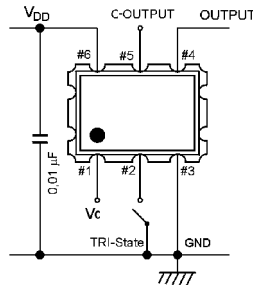
Suggested soldering pad:



Reflow soldering condition:

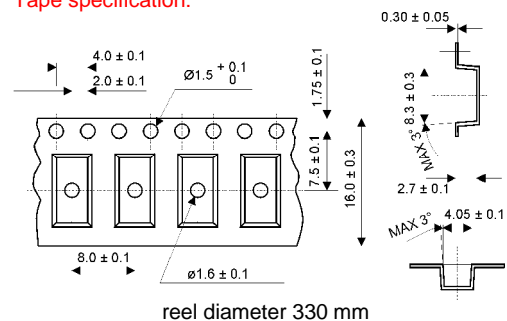


Test circuit:



pin #2	pin #4 / pin #5
"L" (0 V)	HIGH IMPEDANCE (Z)
"H" (+3.3 V) or OPEN	OUTPUT

Tape specification:



PIN	CONNECTION
1	VC
2	Tri-state
3	GND
4	Output
5	C-Output
6	V _{DD}